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Practitioner's Docket No. NVIDP033A/P000873

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: James M. Van Dyke et al.

Application No.: 10/657,957

Group No.: 2186

Filed: September 8, 2003

Examiner: Choi, Woo H.

For: SYSTEM AND METHOD FOR A HIGH BANDWIDTH-LOW LATENCY
MEMORY CONTROLLER

Mail Stop AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

ATTENTION: Board of Patent Appeals and Interferences

APPELLANT'S BRIEF (37 C.F.R. § 41.37)

This brief is in furtherance of the Notice of Appeal, filed in this case on February 18, 2005.

The fees required under § 1.17, and any required petition for extension of time for filing this brief and fees therefor, are dealt with in the accompanying TRANSMITTAL OF APPEAL BRIEF.

This brief contains these items under the following headings, and in the order set forth below (37 C.F.R. § 41.37(c)(i)):

I	REAL PARTY IN INTEREST			
II	RELATED APPEALS AND INTERFERENCES	04/20/2005 DHALL1	00000004 501351	10657957
III	STATUS OF CLAIMS	01 FC:1402	500.00 DA	
IV	STATUS OF AMENDMENTS			
V	SUMMARY OF CLAIMED SUBJECT MATTER			
VI	ISSUES			
VII	ARGUMENTS			
VIII	APPENDIX OF CLAIMS INVOLVED IN THE APPEAL			